

<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	09/964,323	YANG, TAI-HER	
	Examiner Kevin Pyo	Art Unit 2878	<i>pw</i>

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to the amendment filed 3/22/2004.
  2.  The allowed claim(s) is/are 24-47.
  3.  The drawings filed on 28 September 2001 are accepted by the Examiner.
  4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
    - a)  All
    - b)  Some\*
    - c)  None
 of the:
    1.  Certified copies of the priority documents have been received.
    2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- \* Certified copies not received: \_\_\_\_\_.
- Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**
5.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
    - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of
 Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
  7.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

Kevin Pyo  
Primary Examiner  
Art Unit: 2878

**EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Benjamin Urcia on 4/13/2004.

The application has been amended as follows in view of providing a proper dependency for claim 47 and correcting minor informalities:

**IN THE CLAIM**

(1) Rewrite claim 47 as follows:

47. (currently amended) A drive circuit for a high input resistance transistor device as claimed in claim 46, further comprising a secondary electricity storage device connected in parallel with the second inverse polarity photoelectric conversion device or for storing said negative drive voltage.

~~A slave negative voltage supply device connected between the gate and source of the high input resistance transistor device, wherein the slave negative voltage supply device stored a portion of the positive drive voltage and, upon cut-off of the positive drive voltage, supplies a negative voltage to the gate of the high input resistance transistor in order to improve cut-off characteristics of the high input resistance transistor.~~

***Allowable Subject Matter***

2. Claims 24-47 are allowed.

3. The following is an examiner's statement of reasons for allowance:

The prior art fails to disclose or make obvious a drive circuit for a high input resistance transistor device comprising, in addition to the other recited features of the claim, the details and function of the recited slave negative voltage supply device and its interconnection with the high input resistance transistor device as claimed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Koomen is cited for disclosing a MOS power-on reset circuit.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Pyo whose telephone number is (571) 272-2445. The examiner can normally be reached on Mon-Fri (with flexible hour), First Mon. off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David P. Porta can be reached on (571) 272-2444. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Kevin Pyo  
Primary Examiner  
Art Unit 2878

Pkk  
4/18/04